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				Application Number	10/766,429		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Filing Date	January 28, 2004		
				First Named Inventor	Johannes K. Notthoff 2811		
				Art Unit			
				Examiner Name			
Sheet	1	of	1	Attorney Docket Number	TRWP123US		

	NON PATENT LITERATURE DOCUMENTS							
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²					
ρL	,	LIN WU, et al.; A Low Jitter 1.25GHz CMOS Analog PLL for Clock Recovery; 0-7803-4455-3/98; IEEE, 1998;						
bL	/	G. VAN DER PLAS, et al.; Systematic Design of a 14-bit 150-MS/s CMOS Current-Steering D/A Converter; Department of Electrical Engineering, Katholieke Universiteit Leuven, ESAT-MICAS; Heverlee, Belgium						

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